

#4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

JAI-YOUNG PARK

Serial No.: *to be assigned*

Examiner: *to be assigned*

Filed: 10 January 2001

Art Unit: *to be assigned*

For: ADD/DROP CROSS CONNECTION APPARATUS FOR SYNCHRONOUS  
DIGITAL HIERARCHY



**INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner  
for Patents  
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. §1.56, and §§1.97 and 1.98 as amended, Applicant cites,  
provides copies and discusses the following art references:

- U.S. Patent No. 6,094,440 to Toshiki Sugawara et al. entitled *Multiplex Type Transmitting Apparatus*;
- U.S. Patent No. 5,914,952 to Doo Seop Eom et al. entitled *Tributary Unit Signal Cross-Connection Apparatus*;
- U.S. Patent No. 5,799,001 to Dong Choon Lee et al. entitled *Composite Network*


*Protective/Recovering Device For Synchronous Digital Hierarchy DXC;*

- U.S. Patent No. 5,777,998 to Giovanni Traverso et al. entitled *Method And Circuit Arrangement For The Realization Of The Higher Path Adaptation/Mapping Function In Synchronous Digital Hierarchy/Optical Network Equipment;*
- U.S. Patent No. 5,574,717 to Masahito Tomizawa et al. entitled *Line Terminating Equipment In SDH Networks, Using Forward Error Correcting Codes;* and
- U.S. Patent No. 5,555,248 to Eiji Sugawara entitled *Tandem Connection Maintenance System.*

The citation of the foregoing references is not intended to constitute an assertion that other or more relevant art does not exist. Accordingly, the Examiner is requested to make a wide-ranging and thorough search of the relative arts.

No fee is incurred by this Statement.

Respectfully submitted,

  
\_\_\_\_\_  
Robert E. Bushnell  
Reg. No.: 27,774

PATENT  
P56284

1522 "K" Street, N.W., Suite 300  
Washington, D.C. 20005  
Area Code: 202-638-5740

Folio: P56284  
Date: 10 January 2001  
I.D.: REB/kf